

Fig. 1

Device Capability Table

Node #0	Reception	ATSC-HD/SD, DSS-HD, Analog
Node #1	Transmission	None
	Conversion	None
	Reception	DSS-HD/SD
Node #2	Transmission	DSS-HD/SD, Analog
	Conversion	DSS-HD/SD -> Analog
Node #3	Reception	ATSC-HD/SD, DSS-HD/SD, Analog
	Transmission	ATSC-HD/SD, DSS-HD/SD, Analog
	Conversion	None
Node #4	Reception	DV-SD, Analog
	Transmission	DV-SD, Analog
	Conversion	None
Node #5	Reception	DV-SD
	Transmission	ATSC-SD
	Conversion	DV-SD -> ATSC-SD

Fig. 2

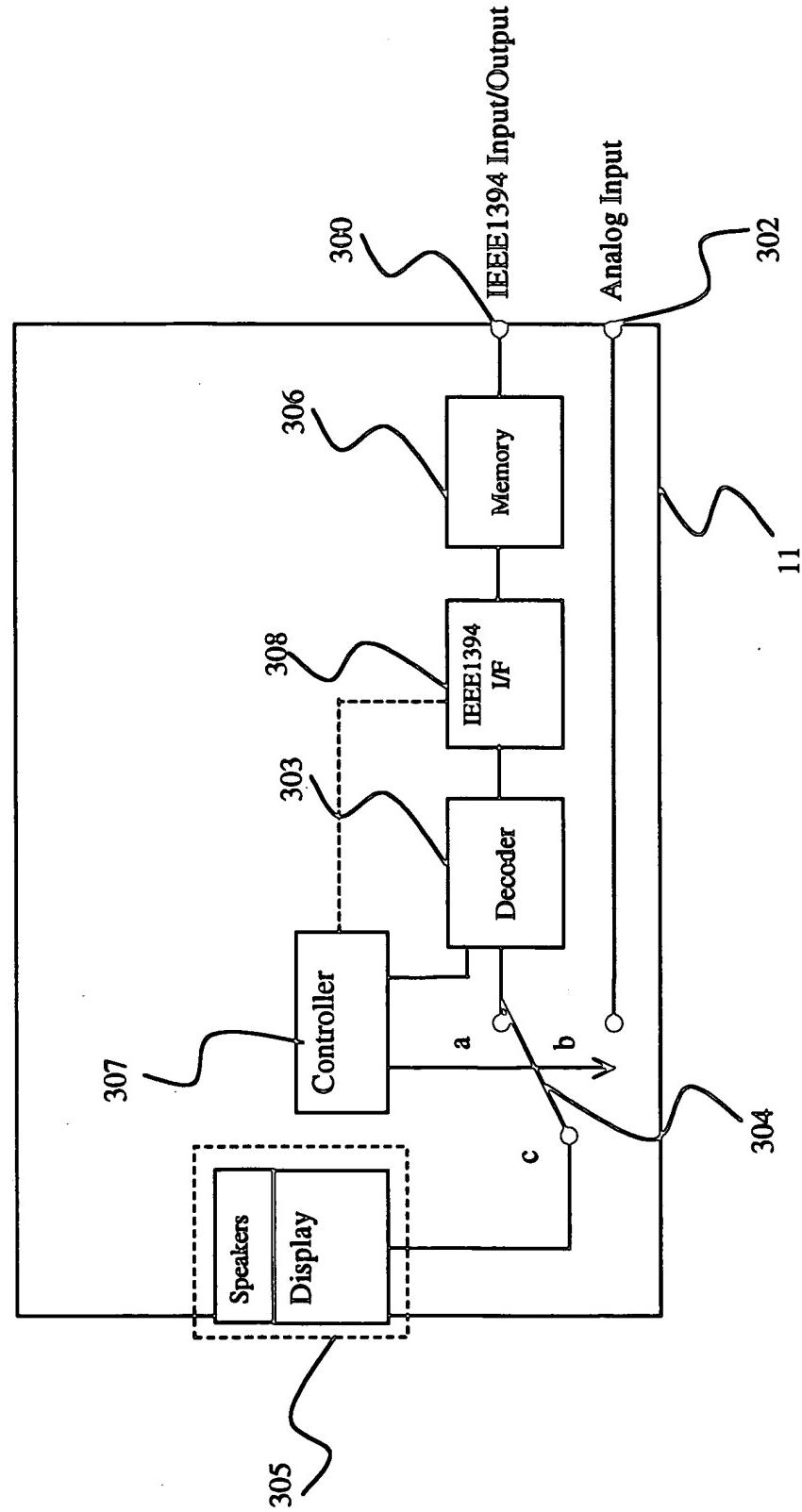


Fig. 3

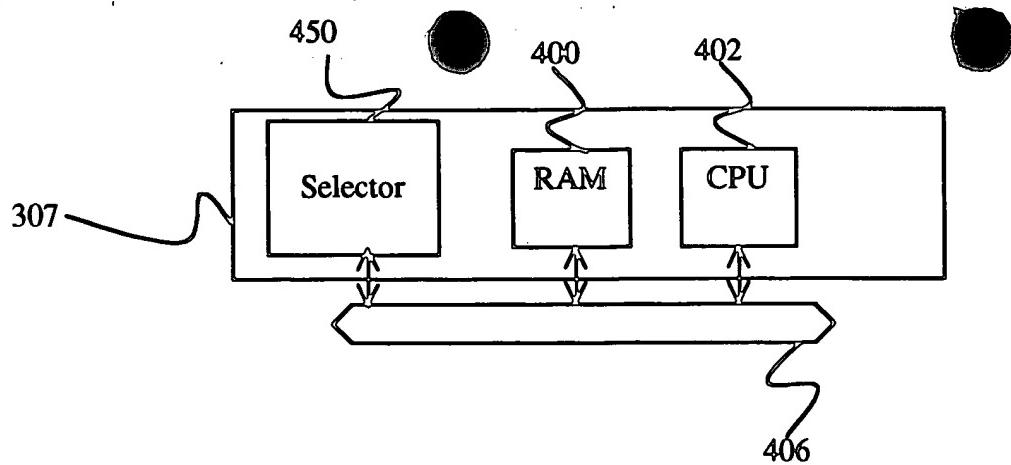


Figure 4A

IEEE1394 Input/Output

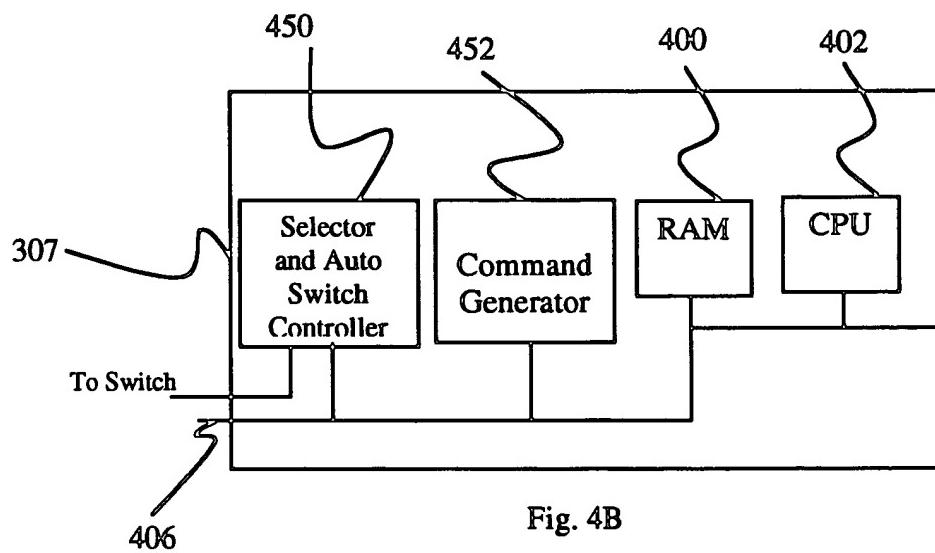
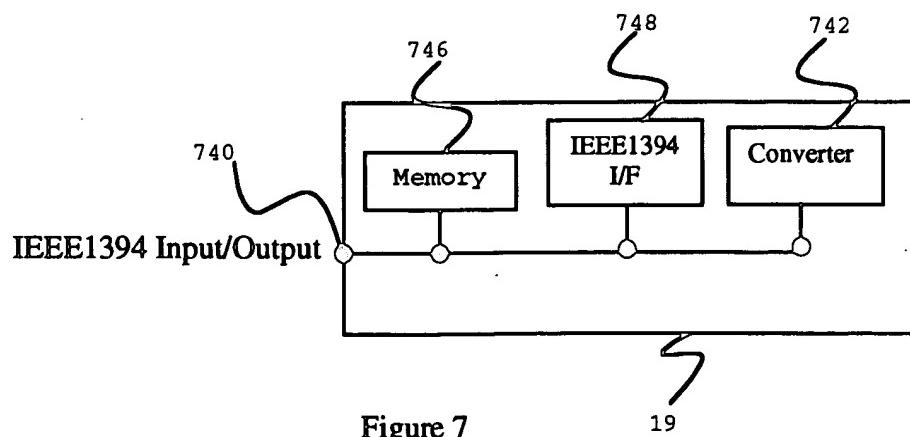
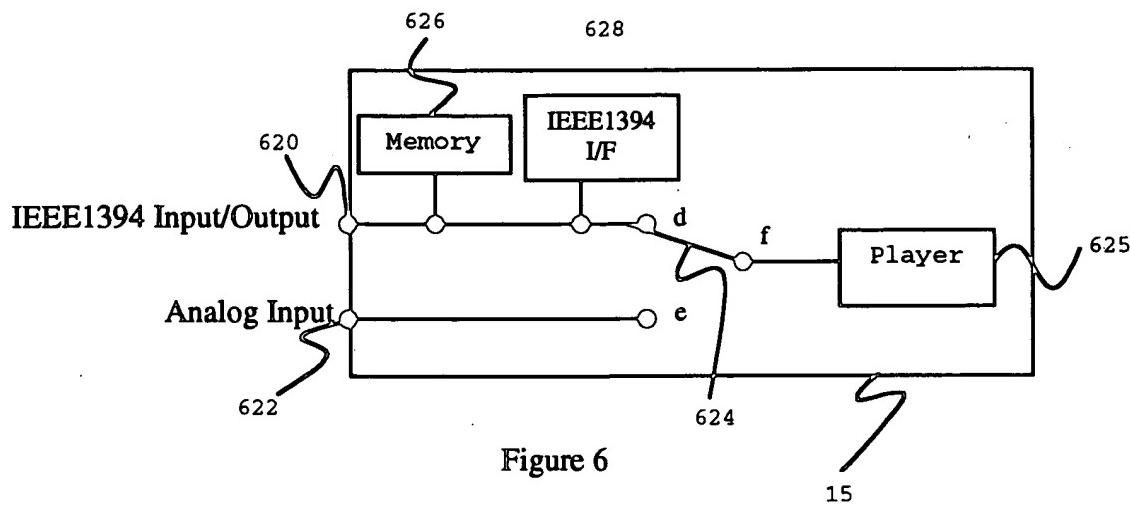
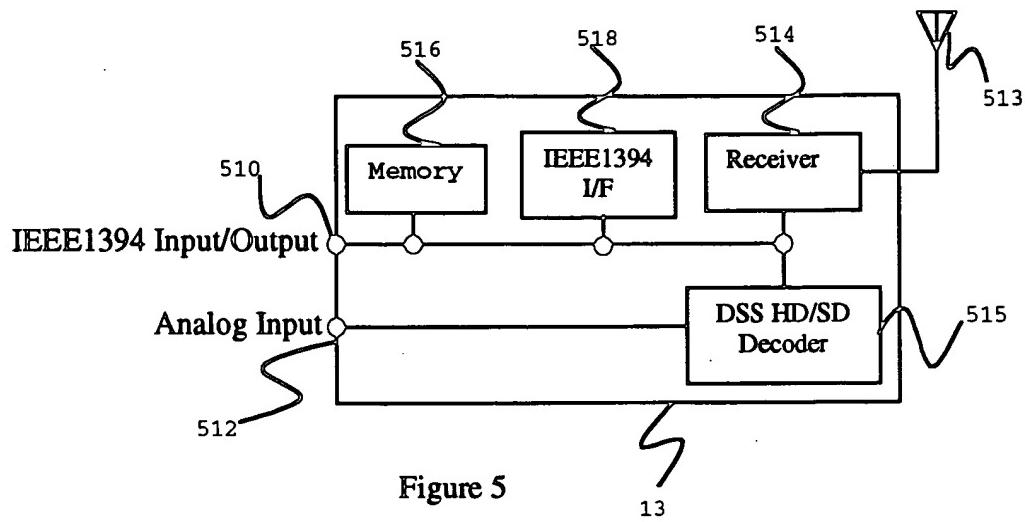


Fig. 4B

IEEE1394 Input/Output



Exemplary Path Table

Source	Format	Route
node #0	N/A	
node #1	DSS-HD	#1 -(Digital) -#0
	DSS-SD	#1 -(Analog) -#0
node #2	ATSC-HD/SD	#2 -(Digital) -#0
	DSS-HD	#2 -(Digital) -#0
	DSS-SD	#2 -(Digital) -#1 -(Analog) -#0
	Analog	#2 -(Analog) -#0
node #3	DV-SD	#3 -(Digital) -#4 -(Digital) -#0
node #4	N/A	

Fig. 8

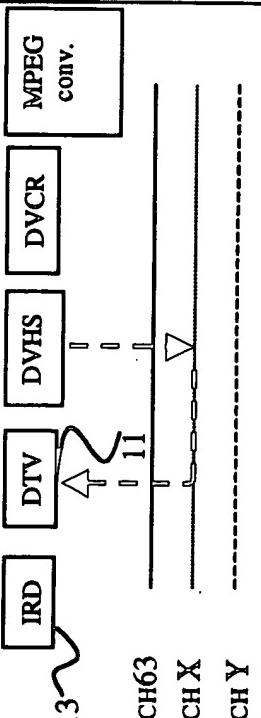
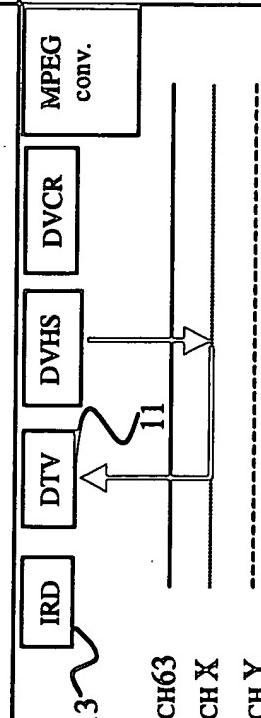
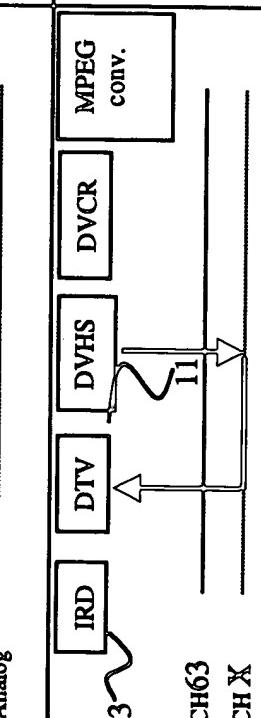
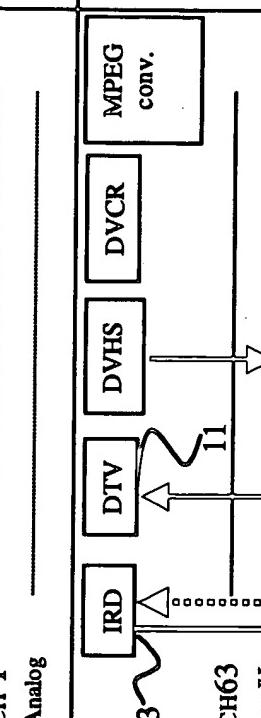
Situation	Step	Action by User	Signal Flow	IRD	DTV	DVHS
Watch DVHS [DSS-SD]	1	DTV “Input Select to DVHS”		Establish P-to-P with DVHS1, Input from CH-X		
	2	DVHS “PLAY”		Play DSS-SD contents, Output TS to CH-X	Recognize SD stream	
	3			Displays “Change TV’s input to IRD-Analog, Change IRD’s input to DVHS-Digital”		
	4	IRD “Input Select to DVHS” DTV “Input Select to IRD-Analog”		Overlay P-to-P with DVHS, Input from CH-X, Decode		

Fig. 9

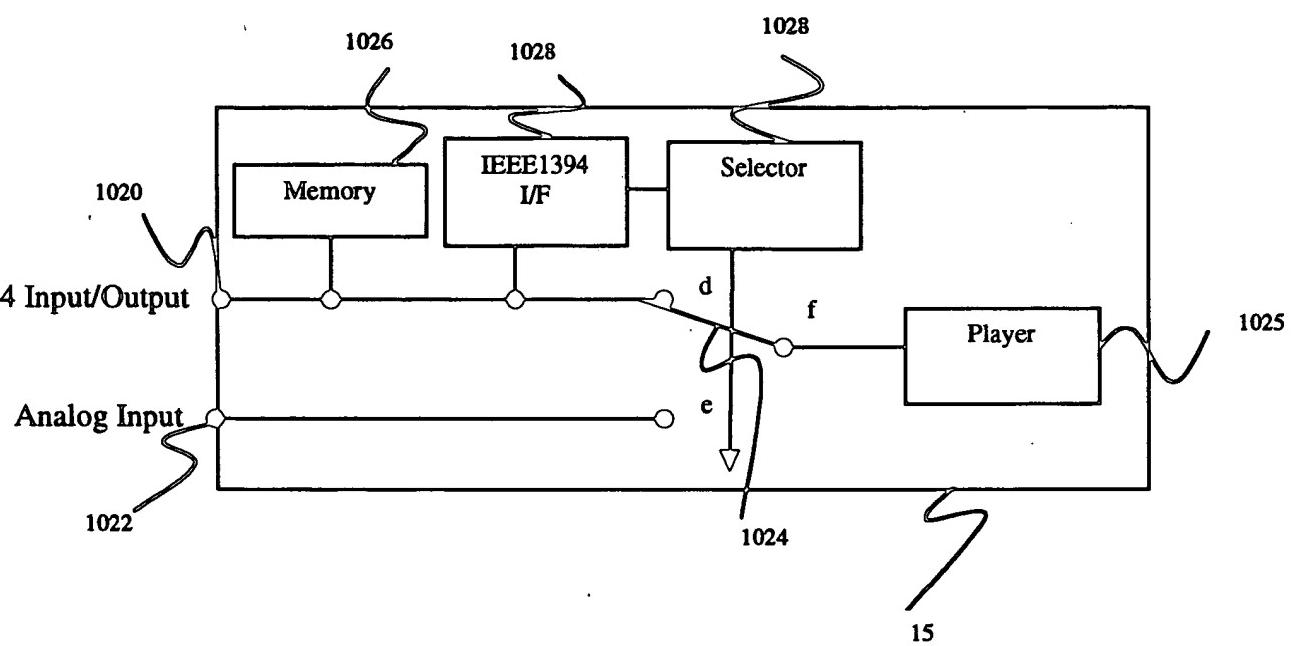


Figure 10